MULTIPHASE ENCODED PROTOCOL AND SYNCHRONIZATION OF BUSES

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ABSTRACT OF THE DISCLOSURE

A multiphase encoded protocol has sufficient density of commands to allow a rich language to be realized on a bus. When ten field bits are dedicated to commands, it is possible to have more than six million words to choose from per clock. Architecture to implement the multiphase encoded protocol and synchronize the bus includes an extracted clock, a command element, and a data element. One-bit multipliers are used as correlation elements to provide feedback into slaved delay-locked loop (DLL) devices, which provides precise phase alignment for successful data extraction of several channels.

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